AMENDMENTS TO THE SPECIFICATION

Please replace Paragraphs [0025]~[0026] with the following paragraphs rewritten in amendment format:

[0025]   Fig. 2 is a block diagram of a portion of an example of a pixel-differentiated CCD image sensor 200 corresponding to the image sensor 102. Details of various pixel-differentiated CCD image sensors can be found in a copending related application by the same inventors (Attorney Docket No. 10018582-1, U.S. patent application number 10/648391, ~~<HDP#6215-0000676>~~, filed the same day as the present application and entitled "Pixel-Differentiated CCD Architecture"), the entirety of which is hereby incorporated by reference. Only a portion of the pixel-differentiated image sensor 200 is shown in order to simplify the depiction. There are further simplifications in Fig. 2, as will be discussed below.

[0026]   The image sensor 200 includes a first plurality of photosensing pixels, the majority of which can be classified as a first type of photo-sensing pixel 202 (hereinafter also referred to as a Type I pixel). Here, the first plurality further includes pixels that can be classified as a second type of photo-sensing pixel 204 (hereinafter also referred to as a Type II pixel). The Type I pixels 202 can be thought of as non-sampling pixels. In contrast, the Type II pixels can be thought of as sampling pixels. Physically, the Type I pixels 202 and the Type II pixels 204 are the same but for the addressing and control lines going ~~the~~ to them, respectively, which give rise to their different classifications. More about the differences between Type I (non-sampling) and Type II (sampling) pixels will be discussed below. Alternatively, additional types of pixels can be provided on the image sensor 200.

Please replace Paragraph [0029] with the following paragraph rewritten in amendment format:

[0029]   Each bank ~~204,~~ 206ᵢ is organized into arrays, each array being either a sampling array 216 or a non-sampling array 218. A sampling array 216 includes a Type II pixel 204 at the bottom, closest to the corresponding HCCD 208ᵢ. The other pixels in

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the array 216 are Type I pixels 202. The non-sampling array 218 includes Type I pixels 202 but does not include any Type II pixels 204.

Please replace Paragraph [0034] with the following paragraph rewritten in amendment format:

[0034]   In a sampling mode, the array 200 is controllable so that only the information in one or more of the Type II pixels 204 is sampled/read. The information in the Type I pixels 202 is not read in the sampling-mode. Depending upon the value of the one or more samples read from the Type II pixels 204, the array 200 is controllable in a read-mode (more detail to be discussed below) so that the information in one or more Type I pixels located within a predetermined area adjacent to or surrounding the one or more Type II pixels, respectively, is read without the need to read all of the Type I pixels. The term "controllable" is used here to connote that the image sensor 102, particularly the array 200, is configured with clocking lines and address/control lines so that the clocking circuit 108 and control logic, e.g., in the ASIC 106, respectively, can control the array 200 to behave according to the sampling-mode or the read-mode.

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